

VISWANADHA INSTITUTE OF TECHNOLOGY AND MANAGEMENT
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Proposed Skill Development Programme on Xilinx

In the Present Scenario VLSI plays a major role for many DSP applications like Processors, Mobile, and Satellite Communications etc. For designing these applications we need a semi custom and full custom tool like Synopsis, Cadence, Mentor Graphics, and Xilinx Tool etc.

So in the present Programme we are designing basic gates using VHDL Programme and that circuits can be implemented by using FPGA and CPLD boards like SPARTAN, VERTEX, ALTERA etc.

Day-1

Date: May 07th 2018

Time: 10 am to 12: 30 pm

1. a) Introduction to Logic Gates
 - And Gate
 - OR Gate
 - XOR Gate
 - Ex- Nor Gate
 - Nand Gate
 - Nor Gate

Hands on Training

- b) Designing structure of
 - Half Adder,
 - Full Adder,
 - Mux,
 - Decoder

Trainers: Mr. K.V. Ganesh
Mrs. R. Roopa Sree
Mrs. T. L Spandana
Ms. M. Devi Sowjanya

Day-2

2. a) Introduction to VHDL
 - What is VHDL
 - Basic Terminology
 - Entity Declaration
 - Architecture Body
 - Configuration Declaration
 - Syntax
 - Identification
 - Data Objects
 - Data Types
 - Operators

b) Hands on Training

Behavioral Modelling & VHDL Programming

Day -3

- Data Flow Modelling and VHDL Programming
- Structure Modelling & VHDL Programming

Resource Person: K.V. Ganesh